line 42, delete "baud" and insert -- symbol --;
line 50, delete "baud" and insert -- symbol --;
line 52, delete "baud" and insert -- symbol --;
line 54, delete "baud" and insert -- symbol --;
line 59, delete "baud" and insert -- symbol --;
line 64, delete "baud" and insert -- symbol --;

line 66, delete "baud" and insert -- symbol --.

Page 11, line 2, delete "baud" and insert -- symbol --;
line 15 and 16, delete "baud" and insert -- symbol --;
line 30, delete "baud" and insert -- symbol --;
line 44, delete "baud" and insert -- symbol --.

Page 12, line 65, delete "baud" and insert -- symbol --.

Page 13, line 33, delete "baud" and insert -- symbol --.

In the Claims:

CANCEL CLAIMS 1-33 AND 41-144

Add the following new claims:

-- 145. An apparatus that is adapted to be coupled to at least one pair of twisted wires that carry a multi-level signal, comprising:

an analog to digital converter for digitally converting the multi-level signal at a particular rate;

a timing recovery circuit for regulating the particular rate at which said analog to digital converter converts the multi-level signal; and,

a digital adaptive equalizer for receiving the digitally converted multi-level signal and selecting one of a plurality of levels. --

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-- 146. The apparatus of claim 145, further comprising an automatic gain control circuit coupled to said analog to digital converter. --

-- 147. The apparatus of claim 146, further comprising a decoder circuit coupled to said digital adaptive equalizer. --

-- 148. The apparatus of claim 147, further comprising a media access controller coupled to said decoder circuit. --

-- 149. The apparatus of claim 145, wherein said digital adaptive equalizer includes a feedforward equalizer, a data slicer and a decision feedback equalizer. --

-- 150. The apparatus of claim 145, wherein said timing recovery circuit regulates the particular rate in accordance with a product of a plurality of peak signal samples. --

-- 151. An apparatus that is adapted to be coupled to at least one pair of twisted wires that carry a multi-level signal transmitted at a transmission rate of at least 25 megasymbols per second, comprising:

an analog to digital converter that is responsive to the multi-level signal transmitted at the transmission rate of at least 25 megasymbols per second;

a clock recovery circuit coupled to said analog to digital converter; and,

a digital adaptive equalizer coupled to said analog to digital converter. --

-- 152. The apparatus of claim 151, further comprising an automatic gain control circuit coupled to said analog to digital converter. --

-- 153. The apparatus of claim 151, further comprising a decoder circuit coupled to said digital adaptive equalizer. --

-- 154. The apparatus of claim 153, further comprising a media access controller coupled to said decoder circuit. --

-- 155. The apparatus of claim 151, wherein said digital adaptive equalizer includes a feedforward equalizer, a data slicer and a decision feedback equalizer. --

-- 156. The apparatus of claim 151, wherein said timing recovery circuit regulates the particular rate in accordance with a product of a plurality of peak signal samples. --

-- 157. An apparatus that is adapted to be coupled to at least one pair of twisted wires that carry a multi-level signal, comprising:

an analog to digital converter;

a clock recovery circuit coupled to said analog to digital converter; and,

a digital adaptive equalizer coupled to said analog to digital converter. --

-- 158. The apparatus of claim 157, further comprising an automatic gain control circuit coupled to said analog to digital converter. --

-- 159. The apparatus of claim 157, further comprising a decoder circuit coupled to said digital adaptive equalizer. --

-- 160. The apparatus of claim 159, further comprising a media access controller coupled to said decoder circuit. --

-- 161. The apparatus of claim 157, wherein said digital adaptive equalizer includes a feedforward equalizer, a data slicer and a decision feedback equalizer. --

-- 162. The apparatus of claim 157, wherein said timing recovery circuit regulates the particular rate in accordance with a product of a plurality of peak signal samples. --

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-- 163. A method for recovering a multi-level signal transmitted on at least one pair of twisted wires, comprising:

converting the multi-level signal to a digital signal at a particular rate;

regulating the particular rate of conversion;

equalizing the digital signal; and,

selecting one of a plurality of levels based on the digital signal. --

-- 164. The method of claim 163, wherein the particular rate is regulated in accordance with a product of a plurality of peak signal samples. --

-- 165. The method of claim 163, wherein the particular rate is at least 25 megasymbols per second. --

-- 166. The method of claim 163, decoding the selected level. --

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By _____

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